



Trizeps IV-WL

XScale PXA270CE CPU Module

SO-DIMM 200

Product Brief April 2009, V1.2

CPU-Module Trizeps IV-WL

- Intel XScale PXA270CE with 312, 416, 520 MHz
- 64 / 128 MByte Low-Power SDRAM
- 32 MByte Strata-FLASH, 32bit buswidth
- NAND Flash option max. 2 GB
- Low-Power WLAN module, 802.11b/g standard / WPA, WPA2, WMMv2,
- 2 UFL antenna connectors, antenna diversity support
- USB: 2 x host, 1 x slave (internal OTG Transceiver)
- PCMCIA/CF card interface
- 3 Serial IF: FF-UART, Bluetooth-UART, IrDA-UART
- SD/MMC card interface, 4Bit SDIO
- Camera Interface
- SPI, I2C
- High efficiency switch-mode core voltage regulator with dynamic core voltage support
- JTAG debug port via FFC-Connector
- Reset generator
- LC-Display controller
- AC97 20 Bit Stereo, low-noise design, programmable sampling rates, headphone out
- AC97 microphone input (mono) for electret-capacitor microphones
- AC97 line input stereo
- AC97 touch interface 4-wire resistive
- AC97 4 x 10 Bit ADC
- 32 Bit bus interface on SO-DIMM 200
- DDR-SODIMM-200 connector, eg. AMP 1376408-1, Trizeps-III compatible

Board-Dimensions 67.6 mm x 36.7 mm x 6 mm, 14.5 gr.

Current-Supply 3.3 V DC, power consumption: t.b.d.

Operating Systems Microsoft Windows Embedded CE 5.0, 6.0