



# Trizeps-MX

## Freescale MX1/MXL CPU Module SO-DIMM 200

Product Brief March 2005, V1.1

### CPU-Module Trizeps-MX

- Freescale MX1 or MXL with 150, 200 MHz
- High efficiency switch-mode core voltage regulator with dynamic core voltage support
- 16 MByte Strata-FLASH, 16bit buswidth
- 64 MByte Low-Power SDRAM
- 2 x UART
- I2C, PWM, USIM (MX1)
- CMOS Camera Interface
- SD/MMC card interface
- LC-Display controller (max QVGA)
- Touch interface 4-wire resistive (MX1)
- Audio Function (MX1)
- Ethernet controller 100/10 MBit 32 bit bus interface
- Parameter EEPROM
- 32 Bit bus interface on SO-DIMM 200
- JTAG debug port via FFC-Connector
- Reset generator
- DDR-SODIMM-200 connector, eg. AMP 1376408-1, Trizeps-III compatible

<b>Board-Dimensions</b>	67.6 mm x 36.7 mm x 6 mm, 14.5 gr.
<b>Current-Supply</b>	3.3 V DC, power consumption: t.b.d.
<b>Operating Systems</b>	Microsoft Windows CE.NET 4.2, 5.0