



Trizeps V

XScale PXA320 CPU Module SO-DIMM 200

Product Brief Nov. 2008, V1.3

CPU-Module Trizeps V

- Marvell XScale PXA320 up to 806 MHz
- 128 or 256 MByte Low-Power DDRAM
- 32 MByte NOR FLASH (16 MByte, auf Anfrage)
- 1 x USB host, 1 x USB OTG incl. Transceiver
- USB 2.0 Highspeed Slave incl. Transceiver
- Compact Flash card interface
- 3 Serial IF: FF-UART, Bluetooth-UART, IrDA(Consumer)-UART
- 2 x SD/MMC/SDIO card interface, 4Bit wide
- 1 x Micro SD Socket on board
- Camera Interface
- I2S, SPI, I2C
- High efficiency switch-mode core voltage regulator with dynamic core voltage support
- JTAG debug port via FFC-Connector
- Reset generator
- LC-Display controller
- AC97 20 Bit Stereo, low-noise design, programmable sampling rates, headphone out
- AC97 microphone input (mono) for electret-capacitor microphones
- AC97 line input stereo
- AC97 touch interface 4-wire resistive
- AC97 4 x 10 Bit ADC
- Ethernet controller 100/10 MBit 32 bit bus interface
- DDR-SODIMM-200 connector (eg. AMP 1376408-1) with 16-bit databus interface and demultiplexed address bus to keep optimum compatibility to Trizeps-III, Trizeps-IV and Trizeps-IV WL series.

Order code	21 020 (128MB DDR), 21 040 (256MB DDR)
Board-Dimensions	67.6 mm x 36.7 mm x 4.8 mm, 11.2 gr. (incl. Ethernet and SD Socket)
Current-Supply	3.3 V DC, average power consumption (incl. Ethernet): 806 MHz, 100% CPU load: 530 mA 806 MHz, 3% CPU load: 270 mA suspend current: t.b.d.
Operating Systems	Microsoft Windows Embedded CE 6.0, CE 5.0