



# Trizeps IV

## XScale PXA270CE CPU Module SO-DIMM 200



Product Brief Nov. 2007, V1.3

### CPU-Module Trizeps-IV

- Intel XScale PXA270CE with 312, 416, 520 MHz
- JTAG debug port via FFC-Connector
- Reset generator
- High efficiency switch-mode core voltage regulator with dynamic core voltage support
- 32 / 64 / (128) MByte Strata-FLASH, 32bit buswidth
- NAND Flash Option 128 MB
- 64 / 128 MByte Low-Power SDRAM
- 4 Serial IF: FF-UART, HW-UART, Bluetooth-UART, IrDA-UART
- I2S, SPI, I2C,
- Camera Interface
- PCMCIA/CF card interface
- SD/MMC card interface, 4Bit SDIO
- LC-Display controller
- AC97 20 Bit Stereo, low-noise design, programmable sampling rates, headphone out
- AC97 microphone input (mono) for electret-capacitor microphones
- AC97 line input stereo
- AC97 touch interface 4-wire resistive
- AC97 4 x 10 Bit ADC
- Ethernet controller 100/10 MBit 32 bit bus interface
- 2 x USB host, onboard OTG transceiver
- Parameter EEPROM
- 32 Bit bus interface on SO-DIMM 200
- DDR-SODIMM-200 connector, Trizeps-III compatible

<b>Board-Dimensions</b>	67.6 mm x 36.7 mm x 6 mm, 14.5 gr.
<b>Current-Supply</b>	3.3 V DC, power consumption: t.b.d.
<b>Operating Systems</b>	Microsoft Windows CE.NET 4.2, 5.0, 6.0